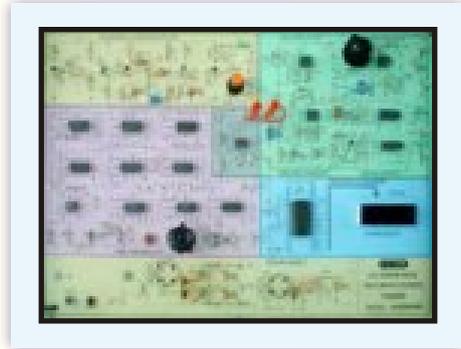# SIGMA

BIT ERROR RATE MEASUREMENT TRAINER
MODEL - BITERR100

This trainer has been designed with a view to provide practical/experimental knowledge of Measurement of Bit Error rate.

SPECIFICATIONS

1.	Power supply requirement	:	230V AC, 50 Hz.
2.	On Board PN sequence Data generator		
	Data Length	:	16 Bits.
	Bit Clock Frequency	:	30 KHz
	Data Format	:	NRZ.
	Time Frame/Bit Burst	:	10000 Bits, 100 Bits and Continues selectable by Switch.
3.	Gaussian Noise	:	0 to 1V rms variable amplitude.
4.	Adder to add PN signal and Gaussian Noise.		
5.	Matched Filter	:	Integrate and Dump Filter
6.	Decision Threshold	:	Comparator and EX-OR circuit.
7.	Error Count Display	:	Seven Segment 4 digit
8.	All parts are soldered on single PCB with complete circuit diagram screen-printed in multicolor.		
9.	Standard Accessories	:	1. A Training Manual
			2. Connecting Patch cords.

EXPERIMENTS

1. To study theory of Gaussian Noise and PN Data.
2. To add Gaussian Noise and PN Data and receive it with matched filter
3. To measure bit error rate, Noise Power, Signal Power.
4. To draw Eb/No graphs for different noise levels.

In keeping view of SIGMA policy of continuous development and improvement, the Specifications may be changed without prior notice or obligation.