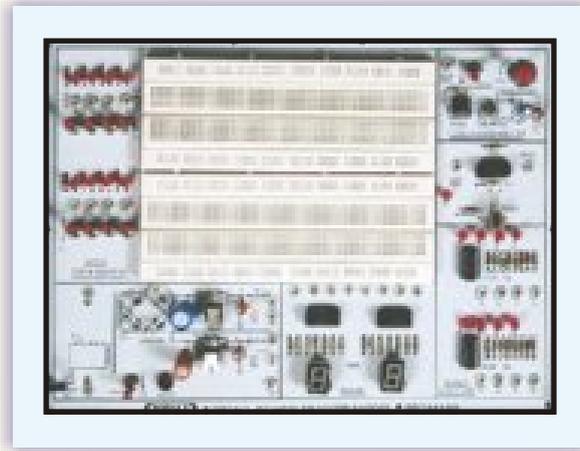




COMPONENT DEVELOPMENT SYSTEM

MODEL CDS100

This trainer has been designed with a view to verify truth table of logic gates and address and to construct small digital circuits and perform various experiments.



SPECIFICATIONS

- | | |
|-------------------------------|---|
| 1. Power supply | : 230V Ac, + 10 % single phase, 50Hz. |
| 2. Built in Power Supply | : +5 VDC, 500mA |
| 3. Pulse generator | : Variable from 1 Hz to 1KHz in 3 ranges for triggering TTL circuits. |
| 4. Breadboard | : 2 strips 175mm long with 1280 tie points |
| 5. High /Low switches | : 8 Nos provided to apply 0 and 1 level |
| 6. Pulsar | : 1 No. provided to apply pulses. |
| 7. Logic Level Indicator LEDs | : 8 Nos. provided for output /input level indication. |
| 8. Display | : 2 No seven segment display LED with decoder 7447 |
| 9. Standard Accessories | : 1. A Instructional Manual. - 1 No.
2. 2mm Connecting jumpers - 10 Nos. |

In keeping view of SIGMA policy of continuous development and improvement, the Specifications may be changed without prior notice or obligation.

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Dealer:-