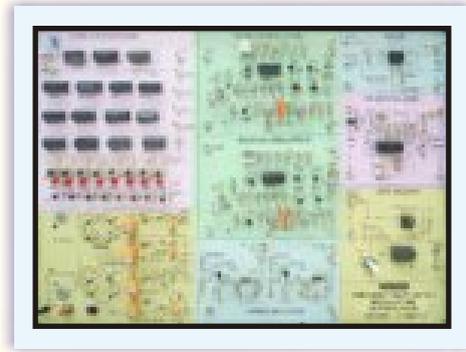




# ASK, FSK, PSK, QPSK AND DPSK GENERATION TRAINER

MODEL - COM114-17A

This trainer has been designed with a view to provide practical / experimental knowledge of ASK, FSK, PSK, QPSK & DPSK Generation technique as practically implemented in Digital Communication systems on a SINGLE PCB.



## SPECIFICATIONS

1. Power supply requirement : 230V AC, 50 Hz.
2. On Board Digital Data generator to generate to any binary input word with Bit clock & Word clock.  
Word Length : 8 Bits.  
Word Clock Frequency : 10 KHz  
Data Format : NRZ, Even Bits I and Odd bits Q signals.
3. On Board Carrier Oscillators  
Frequencies Range : 320KHz & 640KHz.
4. On Board synchronized RF Carrier : 1.28 MHz - Sine & Cosine.
5. Modulator Type : Two Balanced Modulators with adder
6. All parts soldered on single PCB with complete circuit diagram Screen Printed.
7. Standard Accessories : A Training Manual  
Connecting Patch cords.

## EXPERIMENTS

1. To study theory of ASK, FSK, PSK, QPSK and DPSK Generation.
2. To Study Block diagram of ASK, FSK, PSK, QPSK and DPSK Generation.
3. To generate ASK signal.
4. To generate FSK signal.
5. To generate PSK signal.
6. To generate QPSK signal.
7. To generate DPSK signal.

In keeping view of SIGMA policy of continuous development and improvement, the Specifications may be changed without prior notice or obligation.

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