



ASK, FSK, PSK, QPSK AND DPSK DETECTION TRAINER

MODEL - COM114-17B

This trainer has been designed with a view to provide practical / experimental knowledge of ASK, FSK, PSK, QPSK & DPSK Detection technique as practically implemented in Digital Communication systems on a SINGLE PCB.



SPECIFICATIONS

1. Power supply requirement : 230V AC, 50 Hz.
2. Demodulator Type : Balanced Modulator, Diode Detector & Squarer.
3. Carrier and Bit Clock Recovery : By PLL
4. Decision Threshold : Comparator
5. All parts soldered on single PCB with complete circuit diagram Screen Printed.
6. Standard Accessories : A Training Manual
Connecting Patch cords.

EXPERIMENTS

1. To study theory of ASK, FSK, PSK, QPSK and DPSK Detection.
2. To Study Block diagram of ASK, FSK, PSK, QPSK and DPSK Detection.
3. To demodulate ASK signal.
4. To demodulate FSK signal.
5. To demodulate PSK signal.
6. To demodulate QPSK signal.
7. To demodulate DPSK signal.

In keeping view of SIGMA policy of continuous development and improvement, the Specifications may be changed without prior notice or obligation.

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Dealer:-