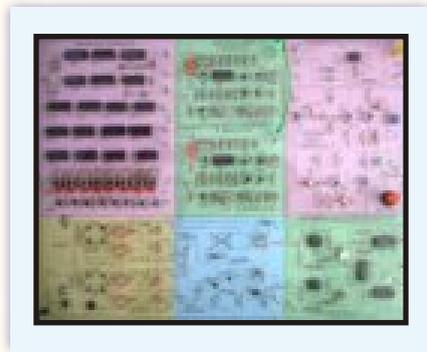




QUADRATURE PHASE SHIFT KEYING MODULATION TRAINER

MODEL - COM117A

This trainer has been designed with a view to provide practical/experimental knowledge of Q.P.S.K. Modulation technique as practically implemented in Digital Communication systems on a SINGLE PCB.



SPECIFICATIONS

1. Power supply requirement : 230V AC, 50 Hz.
2. Built in IC based power supply.
3. On Board Modulating Digital Data signal generator to generate any binary input word with Bit clock & Word clock.
Word Length : 8 Bits.
Word Clock Frequency : 8 KHz to 12 KHz.
Data Format : NRZ, 0° and 90° signals.
4. Modulator Type : Balanced Modulator as Phase modulator.
5. All parts are soldered on single PCB with complete circuit diagram screen-printed.
6. Standard Accessories : 1. A Training Manual
2. Connecting Patch cords

EXPERIMENTS

1. To study theory of QPSK Modulation.
2. To generate QPSK signal.
3. To demodulate QPSK signal using balanced modulators.
4. To observe QPSK modulated output by for different input binary codes.
5. To observe QPSK modulated output by varying the bit clock frequency.
6. To observe PSK modulated output by varying the phase angle and amplitude of carrier signal.

In keeping view of SIGMA policy of continuous development and improvement, the Specifications may be changed without prior notice or obligation.

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