



HAMMING CODES, EVEN AND ODD PARITY

MODEL - COM121L

This trainer has been designed with a view to provide practical & experimental knowledge of effect of noise in digital system and Error detection and Correction on single PCB.



SPECIFICATIONS

1. Built in IC based power supply.
2. On Board Modulating Digital Data signal generator to generate any binary input word with Bit clock & Word Clock.
Word Length : 8 Bits.
3. On Board Parity generator : One bit and Four bits.
4. Data receiver
5. All parts are soldered on single PCB with complete circuit diagram screen-printed in multicolour..
6. Standard Accessories
 1. The manual having practical details.
 2. All the necessary cables.

EXPERIMENTS

1. To insert noise in digital data
2. To generate digital data word (8 Bits) with No parity bits.
3. To generate digital data word with one parity bit. (7 data + 1 Parity)
4. To generate digital data word with four data & four parity check bits.
5. To insert error in any one bit in received data word.
6. To generate & Detect Parity bits and use it to correct error.
7. To detect the error and correct it using Hamming Codes.

In keeping view of SIGMA policy of continuous development and improvement, the Specifications may be changed without prior notice or obligation.

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Dealer:-