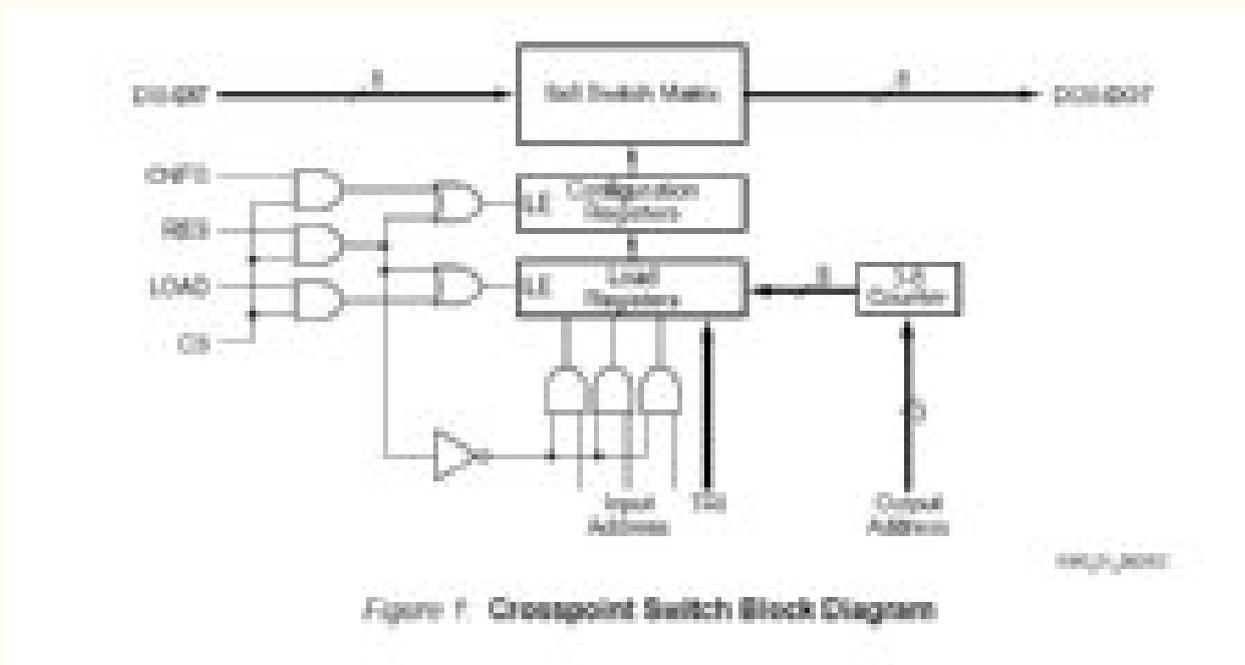# PSTN T/S/T SWITCH HARDWARE SIMULATION TRAINER
MODEL PSTN100S

SIGMA

This trainer has been designed with a view to provide practical and experimental knowledge of PSTN T/S/T Switch Hardware Simulation.

PSTN T-S-T SWITCH CROSS POINT SIMULATION

This software simulation provides a functional description of VHDL source code for a N x N Digital Crosspoint Switch. The code is designed with eight inputs and eight outputs in order to target the 128-macrocell CoolRunner™-II CPLD device but can be easily expanded to target higher density devices.

Figure 1. Crosspoint Switch Block Diagram

In keeping view of SIGMA policy of continuous development and improvement, the Specifications may be changed without prior notice or obligation.