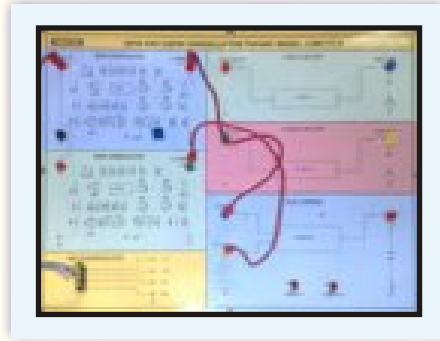




QPSK AND DQPSK DEMODULATION TRAINER

MODEL- COM117F-D

This trainer has been designed with a view to provide practical/experimental knowledge of QPSK and DQPSK Demodulation.



SPECIFICATIONS

1. Receiver Clock : Generated by Phase-Lock Loop.
2. Data Format (decoding) : Dibit Pair (I & Q), Differential Decoding of I & Q Bits
3. Carrier Demodulation : QPSK, DQPSK.
4. Standard Accessories : Experimental and Circuit Description Manual.
Connecting Patch cords. (6 Nos.)

EXPERIMENTS

1. To study theory of QPSK/ DQPSK Modulation & Demodulation.
2. To demodulate QPSK signal.
3. To demodulate DQPSK signal.

In keeping view of SIGMA policy of continuous development and improvement, the Specifications may be changed without prior notice or obligation.

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Dealer:-